Tutorial outline

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What is a language translator?

You type: `cc foo.c... What happens?

Language: Vehicle (architecture) for transmitting information between components of a system. For our purposes, a language is a formal interface. The goal of every compiler is correct and efficient language translation.
The process of language translation

1. A person has an idea of how to compute something:

\[
\text{fact}(n) = \begin{cases} 
1 & \text{if } n \leq 0 \\
 n \times \text{fact}(n-1) & \text{otherwise}
\end{cases}
\]

2. An algorithm captures the essence of the computation:

\[
\text{fact}(n) = \text{if } n \leq 0 \text{ then } 1 \text{ else } n \times \text{fact}(n-1)
\]

Typically, a pseudocode language is used, such as “pidgin ALGOL”.

3. The algorithm is expressed in some programming language:

```c
int fact(int n) {
    if (n <= 0) return(1);
    else return(n*fact(n-1));
}
```

We would be done if we had a computer that “understood” the language directly. So why don’t we build more C machines?

a) How does the machine know it’s seen a C program and not a Shakespeare sonnet?

b) How does the machine know what is “meant” by the C program?

c) It’s hard to build such machines. What happens when language extensions are introduced (C++)?

d) RISC philosophy says simple machines are better.
Finally...

A compiler translates programs written in a source language into a target language. For our purposes, the source language is typically a programming language—convenient for humans to use and understand—while the target language is typically the (relatively low-level) instruction set of a computer.

**Source Program**
```c
main() {
    int a;
    a += 5.0;
}
```

**Target Program (Assembly)**
```assembly
_main:
    !#PROLOGUE# 0
    sethi %hi(LF12),%g1
    add %g1,%lo(LF12),%g1
    save %sp,%g1,%sp
    !#PROLOGUE# 1
    sethi %hi(L2000000),%o0
    ldd [%o0+%lo(L2000000)],%f0
    ld [%fp+-0x4],%f2
    fitod %f2,%f4
    fadd %f4,%f0,%f6
    fDTOi %f6,%f7
    st %f7,[%fp+-0x4]
```

Running the Sun cc compiler on the above source program of 32 characters produces the assembly program shown to the right. The bound binary executable occupied in excess of 24 thousand bytes.
Structure of a compiler

**Front End**

Scanner: decomposes the input stream into tokens. So the string “a += 5.0;” becomes

\[
\text{a} \text{ += 5.0 ;}
\]

Parser: analyzes the tokens for correctness and structure:

\[
\text{+=}
\]

\[
\text{a} \quad 5.0
\]

Semantic analysis: more analysis and type checking:

\[
\text{+=}
\]

\[
\text{a} \quad \text{flt} \rightarrow \text{int}
\]

\[
5.0
\]
Structure of a compiler

Middle End

```
+ =
  a
  flt->int
    5.0
```

The middle end might eliminate the conversion, substituting the integer “5” for the float “5.0”.

Code Generation

The code generator can significantly affect performance. There are many ways to compute “a+=5”, some less efficient than others:

```
while (t ≠ a + 5) do
  t ← rand()
od
a ← t
```

While optimization can occur throughout the translation process, machine-independent transformations are typically relegated to the middle-end, while instruction selection and other machine-specific activities are pushed into code generation.
Bootstrapping a compiler

Often, a compiler is written in it “itself”. That is, a compiler for \textsc{pascal} may be written in \textsc{pascal}. How does this work?

\textbf{Initial Compiler for } \textit{L} \textbf{on Machine } \textit{M} 

1. The compiler can be written in a small subset of \textit{L}, even though the compiler translates the full language.
2. A throw-away version of the subset language is implemented on \textit{M}. Call this compiler \( \alpha \).
3. The \textit{L} compiler can be compiled using the subset compiler, to generate a full compiler \( \beta \).
4. The \textit{L} compiler can also compile itself. The resulting object \( \gamma \) can be compared with \( \beta \) for verification.

\textbf{Porting the Compiler}

1. On machine \textit{M}, the code generator for the full compiler is changed to target machine \textit{N}.
2. Any program in \textit{L} can now be cross-compiled from \textit{M} to \textit{N}.
3. The compiler can also be cross-compiled to produce an instance of \( \gamma \) that runs on machine \textit{N}.

If the run-time library is mostly written in \textit{L}, or in an intermediate language of \( \beta \), then these can also be translated for \textit{N} using the cross-compiler.
What else does a compiler do?

```c
if (p)
    a = b + (c
else {d = f;
q = r;
```
Compiler design points – aquatic analogies

**Powerboat** Turbo–?. These compilers are fast, load-and-go. They perform little optimization, but typically offer good diagnostics and a good programming environment (sporting a good debugger). These compilers are well-suited for small development tasks, including small student projects.

**Sailboat** BCPL, Postscript. These compilers can do neat tricks but they require skill in their use. The compilers themselves are often small and simple, and therefore easily ported. They can assist in bootstrapping larger systems.

**Tugboat** C++ preprocessor, RATFOR. These compilers are actually front-ends for other (typically larger) back-ends. The early implementations of C++ were via a preprocessor.

**Barge** Industrial-strength. These compilers are developed and maintained with a company’s reputation on the line. Commercial systems use these compilers because of their integrity and the commitment of their sponsoring companies to address problems. Increasingly these kinds of compilers are built by specialty houses such as Rational, KAI, etc.

**Ferry** Gnu compilers. These compilers are available via a General Public License from the Free Software Foundation. They are high-quality systems and can be built upon without restriction.

Another important design issue is the extent to which a compiler can respond *incrementally* to changes.
Compilers are taking over the world!

While compilers most prevalently participate in the translation of programming languages, some form of compiler technology appears in many systems:

**Text processing** Consider the “*r-off” text processing pipe:

\[
\text{PIC} \rightarrow \text{TBL} \rightarrow \text{EQN} \rightarrow \text{TROFF}
\]

or the \LaTeX\ pipe:

\[
\LaTeX \rightarrow \TeX
\]

each of which may produce

\[
\text{DVI} \rightarrow \text{PostScript}
\]

**Silicon compilers** Such systems accept circuit specifications and compile these into \text{VLSI} layouts. The compilers can enforce the appropriate “rules” for valid circuit design, and circuit libraries can be referenced like modules in software library.
## Compiler design vs. programming language design

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<th>Programming languages have</th>
<th>So compilers offer</th>
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<td>Dynamic typing</td>
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<table>
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<th>It’s expensive for a compiler to offer</th>
<th>So some languages avoid that feature</th>
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In general, *simple* languages such as C, PASCAL, and SCHEME have been more successful than complicated languages like PL/1 and ADA.
Language design for humans

Procedure $\text{foo}(x, y)$

\begin{verbatim}
declare
  x, y integer
  a, b integer
  *p integer

  p ← rand() ? &a : &b
  *p ← x + y

end
\end{verbatim}

Syntactic simplicity. Syntactic signposts are kept to a minimum, except where aesthetics dictate otherwise: parentheses in C, semicolons in PASCAL.

Resemblance to mathematics. Infix notation, function names.

Flexible internal structures. Nobody would use a language in which one had to predeclare how many variables their program needed.

Freedom from specifying side-effects. What happens when $p$ is dereferenced?

Programming language design is often a compromise between ease of use for humans, efficiency of translation, and efficiency of target code execution.
Language design for machines

We can require much more of our intermediate languages, in terms of details and syntactic form.
Compilers and target instruction sets

How should we translate \( X = Y + Z \)

In the course of its code generation, a simple compiler may use only 20% of a machine’s potential instructions, because anomalies in an instruction set are difficult to “fit” into a code generator.

Consider two instructions

\[
\begin{align*}
\text{ADDReg} & \quad R_1 \quad R_2 \\
& \quad R_1 \leftarrow R_1 + R_2 \\
\text{ADDMem} & \quad R_1 \quad \text{Loc} \\
& \quad R_1 \leftarrow R_1 + \text{Loc}
\end{align*}
\]

Each instruction is **destructive** in its first argument, so \( Y \) and \( Z \) would have to be refetched if needed.

\[
\begin{align*}
\text{LOAD} & \quad 1 \quad Y \\
\text{ADDMem} & \quad 1 \quad Z \\
\text{STORE} & \quad 1 \quad X
\end{align*}
\]

A simpler model would be to do all arithmetic in registers, assuming a **nondestructive** instruction set, with a reserved register for results (say, \( R_0 \)):

\[
\begin{align*}
\text{LOAD} & \quad 1 \quad Y \\
\text{LOAD} & \quad 2 \quad Z \\
\text{LOADReg} & \quad 0 \quad 1 \\
\text{ADDReg} & \quad 0 \quad 2 \\
\text{STORE} & \quad 0 \quad X
\end{align*}
\]

This code preserves the value of \( Y \) and \( Z \) in their respective registers.

A popular approach is to generate code assuming the nondestructive paradigm, and then use an instruction selector to optimize the code, perhaps using destructive operations.
Current wisdom on compilers and architecture

Architects should design “orthogonal” RISC instruction sets, and let the optimizer make the best possible use of these instructions. Consider the program

\[
\text{for } i \leftarrow 1 \text{ to } 10 \text{ do } X \leftarrow A[i]
\]

where \(A\) is declared as a 10-element array \((1 \ldots 10)\).

The \texttt{VAX} has an instruction essentially of the form

\[\text{Index}(A, i, \text{low}, \text{high})\]

with semantics

\[
\begin{align*}
\text{if } (\text{low} \leq i \leq \text{high}) & \text{ then} \\
& \text{return } (A + 4 \times i) \\
\text{else} & \\
& \text{return } (\text{error}) \\
\text{fi}
\end{align*}
\]

However, notice that the loop does not violate the array bounds of \(A\). Moreover, in moving from \(A[i]\) to \(A[i + 1]\), the new address can be calculated by adding 4 to the old address.

While the use of an Index instruction may seem attractive, better performance can be obtained by providing smaller, faster instructions to a compiler capable of optimizing their use.

Internally, this instruction requires two tests, one multiplication, and one addition.